

#### **SPECIFICATION**

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Customar Nama	×					
Customer Name	-0	- L	<u> </u>			
	GW-8465YX-EB-V1. 0-16strings LiFePO4 Active					
Product Type	Balance Board/16SLFP Active Balance Board					
Customer Type	30h 1	6				
Edition	A0		×			
File No.	"44; V3230K1.	Preparation Date	2020\06\19			
	×	339	Docan Power			
Approval	Audit	Prepared by				
at interest	Yantao	(25)				
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			MMM 36 36 30 K 3			
Date:		Date:	×			



#### 1. Outline

This specification describes the scope of lithium battery protection circuit, electrical performance parameters relevant content, wiringin structions, sizes, and other projects.

All project standards described in this specification can be used as quality inspections tandards and basis.

### 2. Product Applications

Rechargeablelithiumbatteries

## 3. Product appearance and process indicators

No.	. Project		Test methods and tools	Inspection standard
1	Prod	luct appearance	Visually	Protective plate appearance should meet the following requirements: a reasonable wiring, components arranged in neat rows, each pad and welding point no oxidation, no abnormal color, components and PCB board surface clean, no stains, does not affect its commercial value
12 30°		Welding Technology	Visually, with a magnifying glass	Joints sleek, solid and reliable welding, no false welding, Weld, burrs and other welding defects.  FR-4
2	Sheet material	(1 <sup>2</sup> )	~ 90°, 00°, 10°	
	PCB production process		Lead rosin process	
	Finished plate welding process	YOCAL WY 1332	Lead welding process	



# 4. Product electrical performance

Test conditions:room temperature  $25^{\circ}\text{C}$ ; overcurrent protection board and there sistance value is the dynamic parameters of the test individual cell voltages required  $\geqslant$  3. 2V

Project	Symbol	The detailed content	Standard
	VDET1	Single overcharge detection voltage	Ternary/Lithium Iron
Overcharge protection  Cell balance	tVdeti	Overcharge detection delay time	190 /30h
	V <sub>REL1</sub>	Single overcharge release voltage	8617
	VBU	Cell-balance detection voltage	Voltage difference≧0.08V ±10mV
	VBL	Cell-balance release voltage	When the voltage difference is less than 45mV, the equalization delay timing is completed (the delay can be set to 0.5, 1, 2, or 4 hours)
	Iв	Balance current	Maximum 5A (balance current is proportional to the voltage difference between batteries)
Over-discharge protection	VDET2	Overdischarge detection delay time	(0)
	tVdet2	Level 1 overcurrent protection current (battery voltage = 3.4V)	39 \
	VREL2	Single over-discharge detection voltage	\
) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1	IDP1	14 13535	1
Over current protection	tVDET1	Detection delay time	339
	IDP2	Level 2 overcurrent protection current (battery voltage = 3.7V/3C)	9000 9VV
	tVdet2	Detection delay time	M. Mosk
N 8673		Exterior short circuit	× \
短路保护 Short circuit protection	Tshort	Detection delay time	\
	770	Protection of release condition	\



Resistance	Rds	Main loop resistance (battery voltage = 3.4V)	
Current consumption	Idd	Internal consumption during operation of the circuit	250mW
Charging maximum Voltage	Vds	Charging port connected to the charger maximum	V \
Chip maximum Voltage	VDS	Input voltage between each string battery node	DC5V
Continuous Discharge Current	302	Sustainable discharge current	33
Continuous charge current	ONT	Sustainable charging current	12 13 13 15 15 15 15 15 15 15 15 15 15 15 15 15
Ratedcurrent temperature Mosfet		Rated current discharge MOSFET surface temperature	<45°
Temperaturecontrol switch	12.	Close the charging and discharging function at above rated temperature	, and
Chargingreverse protection	w.	When charging, avoid the positive polarity and negative polarity reversal of charging.	NO
PCB Size		PCB Length, Width, Thickness	≦84*65*17mm

# 5. Wiring instructions:

### 5.2, Voltageacquisitionwires:

PCM Board PCB Pads

BO, B1....(B+)

5.3 Wire instruction Diagram

B- and PC- conductors must use high and low temperature resistant silicone wires with a current of  $\geq$ 35A. B0°B+ acquisition lines use one 11P/2.0 and one 6P/2.0 cable.



# 6. Main line connection diagram

